

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
B.Tech. in ELECTRONICS AND COMMUNICATION ENGINEERING
COURSE STRUCTURE & SYLLABUS (R22 Regulations)
Applicable from AY 2022-23 Batch

I Year I Semester

S. No.	Course Code	Course Title	L	T	P	Credits
1.	MA101BS	Matrices and Calculus	3	1	0	4
2.	PH102BS	Applied Physics	3	1	0	4
3.	EC103ES	C Programming for Engineers	3	0	0	3
4.	ME104ES	Engineering Workshop	0	1	3	2.5
5.	EN105HS	English for Skill Enhancement	2	0	0	2
6.	EC106ES	Elements of Electronics and Communication Engineering	0	0	2	1
7.	PH107BS	Applied Physics Laboratory	0	0	3	1.5
8.	EC108ES	C Programming for Engineers Laboratory	0	0	2	1
9.	EN109HS	English Language and Communication Skills Laboratory	0	0	2	1
10.	*MC110	Environmental Science	3	0	0	0
		Induction Programme				
		Total	14	3	12	20

I Year II Semester

S. No.	Course Code	Course Title	L	T	P	Credits
1.	MA201BS	Ordinary Differential Equations and Vector Calculus	3	1	0	4
2.	CH202BS	Engineering Chemistry	3	1	0	4
3.	ME203ES	Computer Aided Engineering Graphics	1	0	4	3
4.	EE204ES	Basic Electrical Engineering	2	0	0	2
5.	EC205ES	Electronic Devices and Circuits	2	0	0	2
6.	EC206ES	Applied Python Programming Laboratory	0	1	2	2
7.	CH207BS	Engineering Chemistry Laboratory	0	0	2	1
8.	EE208ES	Basic Electrical Engineering Laboratory	0	0	2	1
9.	EC209ES	Electronic Devices and Circuits Laboratory	0	0	2	1
		Total	11	3	12	20

II YEAR I SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	MA301BS	Numerical Methods and Complex Variables	3	1	0	4
2	EC302PC	Analog Circuits	3	0	0	3
3	EC303PC	Network analysis and Synthesis	3	0	0	3
4	EC304PC	Digital Logic Design	3	0	0	3
5	EC305PC	Signals and Systems	3	1	0	4
6	EC306PC	Analog Circuits Laboratory	0	0	2	1
7	EC307PC	Digital logic Design Laboratory	0	0	2	1
8	EC308PC	Basic Simulation Laboratory	0	0	2	1
9	*MC309	Constitution of India	3	0	0	0
		Total Credits	18	2	6	20

II YEAR II SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	EC401PC	Probability Theory and Stochastic Processes	3	0	0	3
2	EC402PC	Electromagnetic Fields and Transmission Lines	3	0	0	3
3	EC403PC	Analog and Digital Communications	3	0	0	3
4	EC404PC	Linear and Digital IC Applications	3	0	0	3
5	EC405PC	Electronic Circuit Analysis	3	0	0	3
6	EC406PC	Analog and Digital Communications Laboratory	0	0	2	1
7	EC407PC	Linear and Digital IC Applications Laboratory	0	0	2	1
8	EC408PC	Electronic Circuit Analysis Laboratory	0	0	2	1
9	EC409PC	Real Time Project/ Field Based Project	0	0	4	2
10	*MC410	Gender Sensitization Lab	0	0	2	0
		Total Credits	15	0	12	20

III YEAR I SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	EC501PC	Microcontrollers	3	1	0	4
2	EC502PC	IoT Architectures and Protocols	3	0	0	3
3	EC503PC	Control Systems	3	1	0	4
4	SM504MS	Business Economics & Financial Analysis	3	0	0	3
5		Professional Elective – I	3	0	0	3
6	EC505PC	Microcontrollers Laboratory	0	0	2	1
7	EC506PC	IoT Architectures and Protocols Laboratory	0	0	2	1
8	EN508HS	Advanced English Communication Skills Laboratory	0	0	2	1
9	*MC510	Intellectual Property Rights	3	0	0	0
		Total Credits	18	2	6	20

III YEAR II SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	EC601PC	Antennas and Wave Propagation	3	0	0	3
2	EC602PC	Digital Signal Processing	3	0	0	3
3	EC603PC	CMOS VLSI Design	3	0	0	3
4		Professional Elective - II	3	0	0	3
5		Open Elective – I	3	0	0	3
6	EC604PC	Digital Signal Processing Laboratory	0	0	2	1
7	EC605PC	CMOS VLSI Design Laboratory	0	0	2	1
8	EC606PC	Advanced Communication Laboratory	0	0	2	1
9	EC607PC	Industry Oriented Mini Project/ Internship	0	0	4	2
10	*MC609	Environmental Science	3	0	0	0
		Total Credits	18	0	10	20

Environmental Science in III Yr II Sem Should be Registered by Lateral Entry Students Only.

IV YEAR I SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1	EC701PC	Microwave and Optical Communications	3	1	0	4
2		Professional Elective – III	3	0	0	3
3		Professional Elective – IV	3	0	0	3
4		Open Elective – II	3	0	0	3
5	EC702PC	Professional Practice, Law & Ethics	2	0	0	2
6	EC703PC	Microwave and Optical Communications Laboratory	0	0	4	2
7	EC704PC	Project Stage – I	0	0	6	3
		Total Credits	15	1	10	20

IV YEAR II SEMESTER

S. No.	Course Code	Course Title	L	T	P	Credits
1		Professional Elective – V	3	0	0	3
2		Professional Elective – VI	3	0	0	3
3		Open Elective – III	3	0	0	3
4	EC801PC	Project Stage – II including Seminar	0	0	22	11
		Total Credits	9	0	22	20

MC – Satisfactory/Unsatisfactory*Professional Elective – I**

EC511PE	Computer Organization & Operating Systems
EC512PE	Data Communications and Computer Networks
EC513PE	Electronic Measurements and Instrumentation

Professional Elective – II

EC621PE	Digital Image Processing
EC622PE	Mobile Communications and Networks
EC623PE	Embedded System Design

Professional Elective – III

EC731PE	Radar Systems
EC732PE	CMOS Analog IC Design
EC733PE	Artificial Neural Networks

Professional Elective – IV

EC741PE	Network Security and Cryptography
EC742PE	Satellite Communications
EC743PE	Biomedical Instrumentation

Professional Elective – V

EC851PE	Artificial Intelligence
EC852PE	5G and beyond Communications
EC853PE	Machine learning

Professional Elective – VI

EC861PE	Multimedia Database Management Systems
EC862PE	System on Chip Architecture
EC863PE	Wireless sensor Networks

Open Electives

Open Elective (OE – I)	Open Elective (OE – II)	Open Elective (OE – III)
1. Fundamentals of Internet of Things 2. Principles of Signal Processing 3. Digital Electronics for Engineering	1. Electronic Sensors 2. Electronics for Health Care 3. Telecommunications for Society	1. Measuring Instruments 2. Communication Technologies 3. Fundamentals of Social Networks

MA301BS: NUMERICAL METHODS AND COMPLEX VARIABLES**B.Tech. II Year I Sem.**

L	T	P	C
3	1	0	4

Pre-requisites: Mathematics courses of first year of study.**Course Objectives:** To learn

- Expressing periodic function by Fourier series and a non-periodic function by Fourier transforms
- Various numerical methods to find roots of polynomial and transcendental equations.
- Concept of finite differences and to estimate the value for the given data using interpolation.
- Evaluation of integrals using numerical techniques
- Solving ordinary differential equations of first order using numerical techniques.
- Differentiation and integration of complex valued functions.
- Evaluation of integrals using Cauchy's integral formula and Cauchy's residue theorem.
- Expansion of complex functions using Taylor's and Laurent's series.

Course outcomes: After learning the contents of this paper the student must be able to

- Express any periodic function in terms of sine and cosine
- Find the root of a given polynomial and transcendental equations.
- Estimate the value for the given data using interpolation
- Find the numerical solutions for a given first order ODE's
- Analyze the complex function with reference to their analyticity, integration using Cauchy's integral and residue theorems
- Taylor's and Laurent's series expansions in complex function

UNIT-I: Fourier Series & Fourier Transforms:**10 L**

Fourier series - Dirichlet's Conditions - Half-range Fourier series - Fourier Transforms: Fourier Sine and cosine transforms - Inverse Fourier transforms.

UNIT-II: Numerical Methods-I**10 L**

Solution of polynomial and transcendental equations: Bisection method, Iteration Method, Newton-Raphson method and Regula-Falsi method. Jacobi and Gauss-Seidal iteration methods for solving linear systems of equations.

Finite differences: forward differences, backward differences, central differences, symbolic relations and separation of symbols, Interpolation using Newton's forward and backward difference formulae. Central difference interpolation: Gauss's forward and backward formulae, Lagrange's method of interpolation.

UNIT-III: Numerical Methods-II**8 L**

Numerical integration: Trapezoidal rule and Simpson's $1/3^{\text{rd}}$ and $3/8^{\text{th}}$ rules.

Ordinary differential equations: Taylor's series, Picard's method, Euler and modified Euler's methods, Runge-Kutta method of fourth order for first order ODE

UNIT-IV: Complex Differentiation**10 L**

Limit, Continuity and Differentiation of Complex functions. Cauchy-Riemann equations (without proof), Milne-Thomson methods, analytic functions, harmonic functions, finding harmonic conjugate, elementary analytic functions (exponential, trigonometric, logarithm) and their properties. (All theorems without Proofs), Conformal mappings, Mobius transformations.

UNIT-V: Complex Integration:**10 L**

Line integrals, Cauchy's theorem, Cauchy's Integral formula, zeros of analytic functions, singularities, Taylor's series, Laurent's series, Residues, Cauchy Residue theorem.

and their properties. (All theorems without Proofs)

TEXT BOOKS:

1. B.S. Grewal, Higher Engineering Mathematics, Khanna Publishers, 36th Edition, 2010.
2. S.S. Sastry, Introductory methods of numerical analysis, PHI, 4th Edition, 2005.

REFERENCE BOOKS:

1. M. K. Jain, S.R.K. Iyengar, R.K. Jain, Numerical methods for Scientific and Engineering Computations, New Age International publishers.
2. Erwin Kreyszig, Advanced Engineering Mathematics, 9th Edition, John Wiley & Sons, 2006.
3. J. W. Brown and R. V. Churchill, Complex Variables and Applications, 7th Edition, Mc-Graw Hill, 2004.

EC302PC: ANALOG CIRCUITS**B.Tech. II Year I Sem.**

L	T	P	C
3	0	0	3

Pre-requisite: Electronic Devices and Circuits**Course Objectives:**

1. Learn the concepts of, load line analysis and biasing techniques
2. Learn the concepts of high frequency analysis of transistors.
3. To give understanding of various types of amplifier circuits
4. Learn the concepts of small signal analysis of BJT and FET
5. To familiarize the Concept of feedback in amplifiers so as to differentiate between negative and positive feedback.

Course Outcomes: Upon completing this course, the students will be able to

1. Design the amplifiers with various biasing techniques.
2. Design single stage amplifiers using BJT and FET
3. Design multistage amplifiers and understand the concepts of High Frequency Analysis of BJT.
4. Utilize the Concepts of negative feedback to improve the stability of amplifiers and positive feedback to sustained oscillations.

Course	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	2	3	3	2	-	-	-	-	-	-	-	1
CO2	2	3	3	2	-	-	-	-	-	-	-	1
CO	2	3	3	2	-	-	-	-	-	-	-	1
CO4	2	3	3	2	-	-	-	-	-	-	-	1

UNIT - I

BJT Biasing: Transistor Biasing and Stabilization - Operating point, DC & AC load lines, Biasing - Fixed Bias, Self Bias, Bias Stability, Bias Compensation using Diode

Analysis and Design of Small Signal Low Frequency BJT Amplifiers: Transistor Hybrid model, Determination of h-parameters from transistor characteristics, Typical values of h- parameters in CE, CB and CC configurations, Transistor amplifying action, Analysis of CE, CC, CB Amplifiers and CE Amplifier with emitter resistance, low frequency response of BJT Amplifiers, effect of coupling and bypass capacitors on CE Amplifier.

UNIT - II

FET- Biasing Techniques

FET Amplifiers: Analysis of CS, CD, CG JFET Amplifiers, comparison of performance with BJT Amplifiers, Basic Concepts of MOSFET Amplifiers, MOS Small signal model, Common source amplifier with resistive, Diode connected and Current source loads, Source follower, Common Gate Stage, Cascode and Folded Cascode Amplifier – frequency response.

UNIT - III

Multistage Amplifiers: Classification of Amplifiers, Distortion in amplifiers, Different coupling schemes used in amplifiers, Frequency response and Analysis of multistage amplifiers, Cascade RC Coupled amplifiers, Cascode amplifier, Darlington pair.

Transistor at High Frequency: Hybrid π -model of Common Emitter transistor model, f_{α} , f_{β} and unity gain bandwidth, Gain-bandwidth product.

UNIT - IV

Feedback Amplifiers: Concepts of feedback – Classification of feedback amplifiers – General characteristics of Negative feedback amplifiers – Effect of Feedback on Amplifier characteristics –

Voltage series, Voltage shunt, Current series and Current shunt Feedback configurations – Simple problems.

UNIT - V

Oscillators: Condition for Oscillations, RC type Oscillators-RC phase shift and Wien-bridge Oscillators, LC type Oscillators –Generalized analysis of LC Oscillators, Hartley and Colpitts Oscillators, Frequency and amplitude stability of Oscillators, Crystal Oscillator.

TEXT BOOKS:

1. Jacob Millman, Christos C Halkias -Integrated Electronics, McGraw Hill Education.
2. Robert L. Boylestead, Louis Nashelsky -Electronic Devices and Circuits theory, 11th Edition, 2009, Pearson

REFERENCE BOOKS:

1. David A. Bell – Electronic Devices and Circuits, 5th Edition, Oxford.
2. Adel S. Sedra, Kenneth C. Smith- Microelectronic Circuits- Theory and Applications, Oxford.
3. Chinmoy Saha, Arindam Halder, Debaati Ganguly -Basic Electronics-Principles and Applications, 2018, Cambridge.

EC303PC: NETWORK ANALYSIS AND SYNTHESIS**B.Tech. II Year I Sem.**

L	T	P	C
3	0	0	3

Course Objectives:

1. To understand the basic concepts on RLC circuits.
2. To know the behavior of the steady state and transient states in RLC circuits.
3. To understand the two port network parameters.
4. Learn the design concepts of various filters and attenuators

Course Outcomes: Upon successful completion of the course, students will be able to:

1. Gain the knowledge on basic RLC circuits behaviour.
2. Analyse the Steady state and transient analysis of RLC Circuits.
3. Characterization of two port network parameters.
4. Analyse the Design aspect of various filters and attenuators

Course	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	2	1	-	-	-	1	-	-	-	-	1
CO2	2	3	2	-	-	-	1	-	-	-	-	1
CO3	3	2	1	-	-	-	-	-	-	-	-	1
CO4	2	3	3	-	-	-	1	-	-	-	-	1

UNIT - I

Network Topology: Basic cutset and tie set matrices for planar networks, Magnetic Circuits, Self and Mutual inductances, dot convention, impedance, reactance concept, Impedance transformation and coupled circuits, co-efficient of coupling, equivalent T for Magnetically coupled circuits, Ideal Transformer.

UNIT - II

Transient and Steady state analysis: RC, RL and RLC Circuits, Sinusoidal, Step and Square responses. RC Circuits as integrator and differentiators. 2nd order series and parallel RLC Circuits, Root locus, damping factor, over damped, under damped, critically damped cases, quality factor and bandwidth for series and parallel resonance, resonance curves.

UNIT - III

Two port network parameters: Z, Y, ABCD, h and g parameters, Characteristic impedance, Image transfer constant, image and iterative impedance, network function, driving point and transfer functions – using transformed (S) variables, Poles and Zeros. Standard T, π , L Sections, Characteristic impedance, image transfer constants, Design of Attenuators, impedance matching network.

UNIT-IV

Filters: Classification of Filters, Filter Networks, Constant-K Filters-Low pass, high pass, Band pass, band-stop filters, M-derived Filters- T and π filters- Low pass, high pass

Attenuators: Types – T, π , L, Bridge T and lattice, Asymmetrical Attenuators T, π , L Equalizers-Types- Series, Shunt, Constant resistance, bridge T attenuation, bridge T phase, Lattice attenuation, lattice Phase equalizers

UNIT – V

Network Synthesis: Driving point impedance and admittance, transfer impedance and admittance, network functions of Ladder and non ladder networks, Poles, Zeros analysis of network functions,

Hurwitz polynomials, Positive Real Functions, synthesis of LC, RC and RL Functions by foster and causer methods.

TEXT BOOKS:

1. Van Valkenburg -Network Analysis, 3rd Ed., Pearson, 216.
2. JD Ryder - Networks, Lines and Fields, 2nd Ed., PHI, 1999.

REFERENCE BOOKS:

1. J. Edminister and M. Nahvi - Electric Circuits, Schaum's Outlines, Mc Graw Hills Education, 1999.
2. A. Sudhakar and Shyammoan S Palli - Networks & Circuits, 4th Ed., Tata McGraw- Hill Publications
3. William Hayt and Jack E. Kimmerley - Engineering Circuit Analysis, 6th Ed., William Hayt and Jack E. Kimmerley, McGraw Hill Company

EC304PC: DIGITAL LOGIC DESIGN**B.Tech. II Year I Sem.**

L	T	P	C
3	0	0	3

Course Objectives:

1. To understand common forms of number representation in logic circuits.
2. To learn basic techniques for the design of digital circuits and fundamental concepts used in the design of digital systems.
3. To understand the concepts of combinational logic circuits and sequential circuits.
4. To understand the Realization of Logic Gates Using Diodes & Transistors.

Course Outcomes: Upon completing this course, the students will be able to

1. Acquire the knowledge on numerical information in different forms and Boolean Algebra theorems.
2. Define Postulates of Boolean algebra and to minimize combinational functions, and design the combinational circuits.
3. Design and analyse sequential circuits for various cyclic functions.
4. Characterize logic families and analyze them for the purpose of AC and DC parameters.

Course	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	2	3	1	2	1	-	-	-	-	-	2
CO2	3	2	2	1	2	1	-	-	-	-	-	2
CO3	2	3	3	2	2	1	-	-	-	-	-	1
CO4	3	2	1	1	1	-	-	-	-	-	-	-

UNIT - I

Number Systems: Number systems, Complements of Numbers, Codes- Weighted and Non-weighted codes and its Properties, Parity check code and Hamming code.

Boolean algebra: Basic Theorems and Properties, Switching Functions- Canonical and Standard Form, Algebraic Simplification, Digital Logic Gates, EX-OR gates, Universal Gates, Multilevel NAND/NOR realizations.

UNIT - II

Minimization of Boolean functions: Karnaugh Map Method - Up to five Variables, Don't Care Map Entries, Tabular Method

Realization of Logic Gates Using Diodes & Transistors: AND, OR and NOT Gates using Diodes and Transistors, DCTL, RTL, DTL, TTL, CML and CMOS Logic Families and its Comparison, standard TTL NAND Gate-Analysis & characteristics, TTL open collector O/Ps, Tristate TTL, MOS & CMOS open drain and tri-state outputs, IC interfacing- TTL driving CMOS & CMOS driving TTL.

UNIT - III

Combinational Logic Circuits: Adders, Subtractors, Comparators, Multiplexers, Demultiplexers, Encoders, Decoders and Code converters, Hazards and Hazard Free Relations.

Sequential Circuits Fundamentals: Basic Architectural Distinctions between Combinational and Sequential circuits, SR Latch, Flip Flops: SR, JK, JK Master Slave, D and T Type Flip Flops, Excitation Table of all Flip Flops, Timing and Triggering Consideration, Conversion from one type of Flip-Flop to another.

UNIT - IV

Registers and Counters: Shift Registers – Left, Right and Bidirectional Shift Registers, Applications of Shift Registers - Design and Operation of Ring and Twisted Ring Counter, Operation of Asynchronous and Synchronous Counters.

Sequential Machines: Finite State Machines, Synthesis of Synchronous Sequential Circuits- Serial Binary Adder, Sequence Detector, Parity-bit Generator, Synchronous Modulo N –Counters.

UNIT – V

Finite state machine: capabilities and limitations, Mealy and Moore models, State equivalence and machine minimization, simplification of incompletely specified machines, Merger graphs. Asynchronous design-modes of operation, Hazards, synthesis of SIC fundamental mode circuits, synthesis of burst mode circuits. Introduction to ASM Charts

TEXT BOOKS

1. Zvi Kohavi & Niraj K. Jha, - Switching and Finite Automata Theory, 3rd Ed., Cambridge, 2010.
2. R. P. Jain - Modern Digital Electronics, 3rd Edition, 2007- Tata McGraw-Hill

REFERENCE BOOKS

1. Morris Mano, Fredriac J. Hill, Gerald R. Peterson - Introduction to Switching Theory and Logic Design –3rd Ed., John Wiley & Sons Inc.
2. Charles H. Roth - Fundamentals of Logic Design, 5th ED., Cengage Learning, 2004.

EC305PC: SIGNALS AND SYSTEMS**B.Tech. II Year I Sem.**

L	T	P	C
3	1	0	4

Course Objectives: The objectives of this subject are to:

1. Classify signals and systems and their analysis in time and frequency domains.
2. Study the concepts of distortion less transmission through LTI systems, convolution and correlation properties.
3. Understand Laplace and Z-transforms their properties for analysis of signals and systems.
4. Identify the need for sampling of CT signals, types and merits and demerits of each type.

Course Outcomes: Upon completing this course the students able to:

1. Characterize various signals, systems and their time and frequency domain analysis, using transform techniques.
2. Identify the conditions for transmission of signals through systems and conditions for physical realization of systems.
3. Use sampling theorem for baseband and band pass signals for various types of sampling and for different duty cycles.
4. Apply the correlation and PSD functions for various applications.

Course	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	-	-	-	-	-	-	-	-	-	1
CO2	3	3	2	-	-	-	-	-	-	-	-	1
CO3	3	3	2	2	-	-	-	-	-	-	-	1
CO4	3	3	2	2	-	-	-	-	-	-	-	1

UNIT - I

Signal Analysis: Analogy between Vectors and Signals, Orthogonal Signal Space, Signal approximation using Orthogonal functions, Mean Square Error, Closed or complete set of Orthogonal functions, Orthogonality in Complex functions, Classification of Signals and systems, Exponential and Sinusoidal signals, Concepts of Impulse function, Unit Step function, Signum function.

UNIT – II

Fourier series: Representation of Fourier series, Continuous time periodic signals, Properties of Fourier Series, Dirichlet's conditions, Trigonometric Fourier Series and Exponential Fourier Series, Complex Fourier spectrum.

Fourier Transforms: Deriving Fourier Transform from Fourier series, Fourier Transform of arbitrary signal, Fourier Transform of standard signals, Fourier Transform of Periodic Signals, Properties of Fourier Transform, Fourier Transforms involving Impulse function and Signum function, Introduction to Hilbert Transform.

UNIT - III

Signal Transmission through Linear Systems: Linear System, Impulse response, Response of a Linear System, Linear Time Invariant(LTI) System, Linear Time Variant (LTV) System, Transfer function of a LTI System, Filter characteristic of Linear System, Distortion less transmission through a system, Signal bandwidth, System Bandwidth, Ideal LPF, HPF, and BPF characteristics, Causality and Paley-Wiener criterion for physical realization, Relationship between Bandwidth and rise time, Convolution and Correlation of Signals, Concept of convolution in Time domain and Frequency domain, Graphical representation of Convolution.

UNIT – IV

Laplace Transforms: Laplace Transforms (L.T), Inverse Laplace Transform, Concept of Region of Convergence (ROC) for Laplace Transforms, Properties of L.T, Relation between L.T and F.T of a signal, Laplace Transform of certain signals using waveform synthesis.

Z-Transforms: Concept of Z- Transform of a Discrete Sequence, Distinction between Laplace, Fourier and Z Transforms, Region of Convergence in Z-Transform, Constraints on ROC for various classes of signals, Inverse Z-transform, Properties of Z-transforms.

UNIT - V

Sampling theorem: Graphical and analytical proof for Band Limited Signals, Impulse Sampling, Natural and Flat top Sampling, Reconstruction of signal from its samples, Effect of under sampling – Aliasing, Introduction to Band Pass Sampling.

Correlation: Cross Correlation and Auto Correlation of Functions, Properties of Correlation Functions, Energy Density Spectrum, Parsevals Theorem, Power Density Spectrum, Relation between Autocorrelation Function and Energy/Power Spectral Density Function, Relation between Convolution and Correlation, Detection of Periodic Signals in the presence of Noise by Correlation, Extraction of Signal from Noise by Filtering.

TEXT BOOKS

1. B.P. Lathi -Signals, Systems & Communications, BSP, 2013.
2. A.V. Oppenheim, A.S. Willsky and S.H. Nawabi -Signals and Systems, 2nd Ed., Prentice Hall

REFERENCE BOOKS

1. Simon Haykin and Van Veen, A. Rama Krishna Rao, -Signals and Systems, TMH, 2008.
2. Michel J. Robert - Fundamentals of Signals and Systems, MGH International Edition, 2008.
3. C. L. Philips, J. M. Parr and Eve A. Riskin -Signals, Systems and Transforms, 3rd Ed., PE, 2004.

EC306PC: ANALOG CIRCUITS LABORATORY**B.Tech. II Year I Sem.**

L	T	P	C
0	0	2	1

Course Outcomes: Upon completing this course the students will be able to

1. Design amplifiers with required Q point and analyse amplifier characteristics
2. Examine the effect multistage amplification on frequency response
3. Investigate feedback concept in amplifiers and oscillator

Course	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	1	-	2	-	3	-	-	3	3	-	-	1
CO2	1	-	2	-	3	-	-	3	3	-	-	1
CO3	1	-	2	-	3	-	-	3	3	-	-	1

List of Experiments (Twelve experiments to be done):

Verify any twelve experiments in H/W Laboratory

1. Perform an experiment to choose Q-point for a Transistor that operate in active region and observe the effect of external Load resistance on Q-point.
2. Design a Self bias Circuit and determine the Q-point of the Transistor and its Stability factor by both simulation and realization with hardware components.
3. Obtain the I/O Characteristics of CE, CB, CC amplifiers. Calculate h-parameters from the Characteristics.
4. Design and Simulate a Common Drain Amplifier with voltage divider bias and determine the Stability factor.
5. Obtain the Drain and Transfer characteristics of CD, CS amplifiers of JFET. Calculate g_m , r_d from the Characteristics.
6. By experiment prove that the voltage gain of Emitter Follower Circuit is one.
7. Design a Common Emitter Amplifier with a gain of 30db and Bandwidth of 10KHZ and plot the frequency response practically.
8. Design a two stage RC Coupled amplifier and prove that gain is increased and analyze the effects of coupling capacitance.
9. Practically prove that the Darlington pair has high input impedance.
10. Draw the high frequency response of common emitter transistor amplifier and calculate f_α , f_β and gain bandwidth product.
11. Design a cascode amplifier for a given specifications
12. Design four topologies of feedback amplifiers and draw the frequency response of them with and without feedback.
13. Design an RC phase shift oscillator circuit and derive the gain condition for oscillations practically for given frequency.
14. Design a Colpitts oscillator circuit for the given frequency and draw the output waveform.

Major Equipment required for Laboratories:

1. Regulated Power Suppliers, 0-30V
2. 20 MHz, Dual Channel Cathode Ray Oscilloscopes.
3. Functions Generators-Sine and Square wave signals
4. Multimeters
5. Electronic devices

EC307PC: DIGITAL LOGIC DESIGN LABORATORY**B.Tech. II Year I Sem.**

L	T	P	C
0	0	2	1

Course Outcomes: Upon completing this course, the students will be able to

1. Acquire the knowledge on numerical information in different forms and Boolean Algebra theorems.
2. Define Postulates of Boolean algebra and to minimize combinational functions, and design the combinational circuits.
3. Design and analyze sequential circuits for various cyclic functions.
4. Characterize logic families and analyze them for the purpose of AC and DC parameters.

Course	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	2	3	1	2	1	-	-	1	-	-	2
CO2	3	2	2	1	2	1	-	-	1	-	-	2
CO3	2	3	3	2	2	1	-	-	1	-	-	1
CO4	3	2	1	1	1	-	-	-	-	-	-	-

List of Experiments

1. Realization of Logic circuit to generate r's Complement using Logic Gates.
2. Realization of given Boolean function using universal gates and minimizing the same. Compare the gate count before and after minimization.
3. Design and realize Full Adder circuit using gates/universal gates. Implement Full Subtractor using full adder.
4. Designing a 2 – bit Comparator using AND, OR and NOT gates. Realize 4 – bit Comparator using 2 – bit Comparators.
5. Realize 2:1 MUX using the given gates and Design 8:1 using 2:1 MUX.
6. Implement the given Boolean function using the given MUX(ex: code converters).
7. Realize a 2x4 Decoder using logic gates and implement 3x8 Decoder using 2x4 Decoder.
8. Implement the given Boolean function using given Decoders.
9. Convert Demultiplexer to Decoder and vice versa.
10. Verification of truth tables of flipflops using different clocks (level triggering, positive and negative edge triggering) also converts the given flipflop from one type to other.
11. Designing of Universal n-bit shift register using flipflops and Multiplexers. Draw the timing diagram of the Shift Register.
12. Design a Synchronous binary counter using D-flipflop/given flipflop.
13. Design a asynchronous counter for the given sequence using given flipflops.
14. Designing of MOD 8 Counter using JK flipflops.
15. Designing of sequence detecting State Machine with minimal states using the given flipflops.
16. Designing of Parity Bit(even/odd) generator using the given flipflops.
17. Realize all logic gates with TTL logic.
18. Realize all logic gates with DTL logic.
 - *Design a sequence detector to detect a given sequence and verify practically
 - *Design a serial subtractor for 4 bit binary numbers

Major Equipment required for Laboratories:

1. 5 V Fixed Regulated Power Supply/ 0-5V or more Regulated Power Supply.
2. 20 MHz Oscilloscope with Dual Channel.
3. Bread board and components/ Trainer Kit.
4. Multimeter.

EC308PC: BASIC SIMULATION LABORATORY**B.Tech. II Year I Sem.**

L	T	P	C
0	0	2	1

Course Outcomes: Upon completing this course, the students will be able to

1. Generate, analyze and perform various operations on Signals/Sequences both in time and Frequency domain
2. Analyze and Characterize Continuous and Discrete Time Systems both in Time and Frequency domain along with the concept of Sampling
3. Generate different Random Signals and capable to analyze their Characteristics
4. Apply the Concepts of Deterministic and Random Signals for Noise removal Applications and on other Real Time Signals

Course	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	2	3	3	3	2	-	-	3	1	-	1
CO2	3	2	3	3	3	2	-	-	3	1	-	1
CO3	3	2	3	3	3	2	-	-	3	1	-	1
CO4	3	2	3	3	3	2	-	-	3	1	-	1

Note:

- All the experiments are to be simulated using MATLAB or equivalent software
- Minimum of 15 experiment are to be completed

List of Experiments:

1. Basic Operations on Matrices.
2. Generation of Various Signals and Sequences (Periodic and Aperiodic), such as Unit Impulse, Unit Step, Square, Saw tooth, Triangular, Sinusoidal, Ramp, Sinc.
3. Operations on Signals and Sequences such as Addition, Multiplication, Scaling, Shifting, Folding, Computation of Energy and Average Power.
4. Finding the Even and Odd parts of Signal/Sequence and Real and Imaginary parts of Signal.
5. Convolution for Signals and sequences.
6. Auto Correlation and Cross Correlation for Signals and Sequences.
7. Verification of Linearity and Time Invariance Properties of a given Continuous/Discrete System.
8. Computation of Unit sample, Unit step and Sinusoidal responses of the given LTI system and verifying its physical realizability and stability properties.
9. Gibbs Phenomenon Simulation.
10. Finding the Fourier Transform of a given signal and plotting its magnitude and phase spectrum.
11. Waveform Synthesis using Laplace Transform.
12. Locating the Zeros and Poles and plotting the Pole-Zero maps in S-plane and Z-Plane for the given transfer function.
13. Generation of Gaussian noise (Real and Complex), Computation of its mean, M.S. Value and its Skew, Kurtosis, and PSD, Probability Distribution Function.
14. Verification of Sampling Theorem.
15. Removal of noise by Autocorrelation / Cross correlation.
16. Extraction of Periodic Signal masked by noise using Correlation.
17. Verification of Weiner-Khinchine Relations.
18. Checking a Random Process for Stationarity in Wide sense.

Major Equipment required for Laboratories:

1. Computer System with latest specifications connected
2. Window Xp or equivalent
3. Simulation software-MAT Lab or any equivalent simulation software

MC309: CONSTITUTION OF INDIA*B.Tech. II Year I Sem.**

L	T	P	C
3	0	0	0

Course Objectives: Students will be able to:

- Understand the premises informing the twin themes of liberty and freedom from a civil rights perspective.
- To address the growth of Indian opinion regarding modern Indian intellectuals' constitutional role and entitlement to civil and economic rights as well as the emergence of nationhood in the early years of Indian nationalism.
- To address the role of socialism in India after the commencement of the Bolshevik Revolution in 1917 and its impact on the initial drafting of the Indian Constitution.

Course Outcomes: Students will be able to:

- Discuss the growth of the demand for civil rights in India for the bulk of Indians before the arrival of Gandhi in Indian politics.
- Discuss the intellectual origins of the framework of argument that informed the conceptualization of social reforms leading to revolution in India.
- Discuss the circumstances surrounding the foundation of the Congress Socialist Party [CSP] under the leadership of Jawaharlal Nehru and the eventual failure of the proposal of direct elections through adult suffrage in the Indian Constitution
- Discuss the passage of the Hindu Code Bill of 1956.

Unit - 1 History of Making of the Indian Constitution- History of Drafting Committee.**Unit - 2** Philosophy of the Indian Constitution- Preamble Salient Features**Unit - 3** Contours of Constitutional Rights & Duties - Fundamental Rights

- Right to Equality
- Right to Freedom
- Right against Exploitation
- Right to Freedom of Religion
- Cultural and Educational Rights
- Right to Constitutional Remedies
- Directive Principles of State Policy
- Fundamental Duties.

Unit - 4 Organs of Governance: Parliament, Composition, Qualifications and Disqualifications, Powers and Functions, Executive, President, Governor, Council of Ministers, Judiciary, Appointment and Transfer of Judges, Qualifications, Powers and Functions**Unit - 5** Local Administration: District's Administration head: Role and Importance, Municipalities: Introduction, Mayor and role of Elected Representative, CEO of Municipal Corporation. Panchayat raj: Introduction, PRI: Zila Panchayat. Elected officials and their roles, CEO ZilaPanchayat: Position and role. Block level: Organizational Hierarchy (Different departments), Village level: Role of Elected and Appointed officials, Importance of grass root democracy**Unit - 6** Election Commission: Election Commission: Role and Functioning. Chief Election Commissioner and Election Commissioners. State Election Commission: Role and Functioning. Institute and Bodies for the welfare of SC/ST/OBC and women.**Suggested Reading:**

1. The Constitution of India, 1950 (Bare Act), Government Publication.
2. Dr. S. N. Busi, Dr. B. R. Ambedkar framing of Indian Constitution, 1st Edition, 2015.
3. M. P. Jain, Indian Constitution Law, 7th Edn., Lexis Nexis, 2014.
4. D.D. Basu, Introduction to the Constitution of India, Lexis Nexis, 2015.