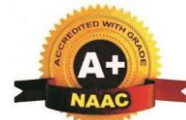




VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

(An Autonomous Institution)

[Sponsored by Lavu Educational Society, Affiliated to JNTUH & Approved by AICTE, New Delhi]
Kondapur (V), Ghatkesar (M), Medchal - Malkajgiri (D) - 501 301. Phone: 96529 10002/3



INFORMATION TECHNOLOGY

COURSE STRUCTURE - VR24

II B. Tech - I Semester

S. No.	Course Code	Course	L	T	P	Credits
1.	EC301ES	Digital Electronics	3	0	0	3
2.	IT302PC	Data Structures	3	0	0	3
3.	MA303BS	Computer Oriented Statistical Methods	3	1	0	4
4.	IT304PC	Computer Organization and Microprocessor	3	0	0	3
5.	IT305PC	Introduction to IoT	2	0	0	2
6.	EC306ES	Digital Electronics Lab	0	0	2	1
7.	IT307PC	Data Structures Lab	0	0	3	1.5
8.	IT308PC	Internet of Things Lab	0	0	3	1.5
9.	IT309PC	Data visualization-R Programming / Power BI	0	0	2	1
10.	*MC310	Gender Sensitization Lab	0	0	2	0
Total			14	1	12	20

Note : L-Theory T-Tutorial P-Practical C-Credits

B.Tech. IT	L	T	P	C
II Year - I Semester	3	0	0	3

DIGITAL ELECTRONICS

Course Objectives:

- To provide a thorough understanding of the binary number system, Boolean algebra, and logic gates.
- To introduce gate-level minimization techniques using Karnaugh maps for circuit simplification.
- To analyze and design combinational logic circuits such as adders, multiplexers, and encoders.
- To explore sequential logic circuits, including flip-flops, counters, and shift registers.
- To understand memory devices, asynchronous sequential circuits, and programmable logic devices.

Course Outcomes: The student will learn

- Apply the principles of Boolean algebra and logic gates to simplify and design digital circuits.
- Use Karnaugh maps and other minimization techniques to optimize logic circuits for efficient hardware implementation.
- Design and analyze combinational logic circuits such as adders, encoders, and multiplexers using digital design methodologies.
- Implement sequential circuits including flip-flops, registers, and counters for various applications.
- Demonstrate knowledge of memory devices, programmable logic, and asynchronous sequential circuits in digital system design.

UNIT - I: BOOLEAN ALGEBRA AND LOGIC GATES:

Digital Systems, Binary Numbers, Number base conversions, Octal and Hexadecimal Numbers, complements, Signed binary numbers, Binary codes, Binary Storage and Registers, Binary logic. Basic Definitions, Axiomatic definition of Boolean Algebra, Basic theorems and properties of Boolean algebra, Boolean functions, canonical and standard forms, other logic operations, Digital logic gates.

UNIT - II: GATE – LEVEL MINIMIZATION:

The map method, Four-variable map, Five-Variable map, product of sums simplification Don't-care conditions, NAND and NOR implementation other Two-level implementations, Exclusive – Or function.

UNIT - III: COMBINATIONAL LOGIC:

Combinational Circuits, Analysis procedure Design procedure, Binary Adder-Subtractor Decimal Adder, Binary multiplier, magnitude comparator, Decoders, Encoders,

Multiplexers, HDL for combinational circuits.

UNIT - IV: SEQUENTIAL LOGIC:

Sequential circuits, latches, Flip-Flops Analysis of clocked sequential circuits, state Reduction and Assignment, Design Procedure. Registers, shift Registers, Ripple counters, synchronous counters, other counters.

UNIT - V: MEMORIES AND ASYNCHRONOUS SEQUENTIAL LOGIC:

Introduction, Random-Access Memory, Memory Decoding, Error Detection and correction Read-only memory, Programmable logic Array programmable Array logic, Sequential Programmable Devices. Introduction, Analysis Procedure, Circuits with Latches, Design Procedure, Reduction of state and Flow Tables, Race-Free state Assignment Hazards, Design Example.

Text Books:

1. Digital Design – Third Edition, M. Morris Mano, Pearson Education/PHI.
2. Digital Principles and Applications Albert Paul Malvino Donald P. Leach TATA McGraw Hill Edition.
3. Fundamentals of Logic Design, Roth, 5th Edition, Thomson.

Reference Books:

1. Switching and Finite Automata Theory by Zvi. Kohavi, Tata McGraw Hill.
2. Switching and Logic Design, C.V.S. Rao, Pearson Education
3. Digital Principles and Design – Donald D.Givone, Tata McGraw Hill, Edition.
4. Fundamentals of Digital Logic and Microcomputer Design, 5TH Edition, M. Rafiquzzaman John Wiley.

B. Tech. IT	L	T	P	C
II Year - I Semester	3	0	0	3

DATA STRUCTURES

Course Objectives:

- Exploring basic data structures such as stacks and queues.
- Introduces a variety of data structures such as hash tables, search trees, tries, heaps, graphs.
- Introduces sorting and pattern matching algorithms

Course Outcomes: The student will learn

- Ability to select the data structures that efficiently model the information in a problem.
- Ability to assess efficiency trade-offs among different data structure implementations or combinations.
- Implement and know the application of algorithms for sorting and pattern matching.
- Design programs using a variety of data structures, including hash tables, binary and general tree structures, search trees, tries, heaps, graphs, and AVL-trees.

UNIT - I:

Introduction to Data Structures, abstract data types, Linear list – singly linked list implementation, insertion, deletion and searching operations on linear list, Stacks-Operations, array and linked representations of stacks, stack applications, Queues-operations, array and linked representations.

UNIT - II:

Dictionaries: linear list representation, skip list representation, operations - insertion, deletion and searching. Hash Table Representation: hash functions, collision resolution-separate chaining, open addressing linear probing, quadratic probing, double hashing, rehashing, extendible hashing.

UNIT - III:

Search Trees: Binary Search Trees, Definition, Implementation, Operations- Searching, Insertion and Deletion, B- Trees, B+ Trees, AVL Trees, Definition, Height of an AVL Tree, Operations – Insertion, Deletion and Searching, Red –Black, Splay Trees.

UNIT - IV:

Graphs: Graph Implementation Methods. Graph Traversal Methods. Sorting: Quick Sort, Heap Sort, External Sorting- Model for external sorting, Merge Sort.

UNIT - V:

Pattern Matching and Tries: Pattern matching algorithms-Brute force, the Boyer –Moore algorithm, the Knuth-Morris-Pratt algorithm, Standard Tries, Compressed Tries, Suffix tries.

Text Books:

1. Fundamentals of Data Structures in C, 2 nd Edition, E. Horowitz, S. Sahni and Susan Anderson Freed, Universities Press.
2. Data Structures using C – A. S.Tanenbaum, Y. Langsam, and M.J. Augenstein, PHI/Pearson Education.

Reference Books:

1. Data Structures: A Pseudocode Approach with C, 2 nd Edition, R. F. Gilberg and B.A.Forouzan, Cengage Learning.

B. Tech. IT	L	T	P	C
II Year - I Semester	3	1	0	4

COMPUTER ORIENTED STATISTICAL METHODS

Course Objectives:

- The theory of Probability, Probability distributions of single and multiple random variables
- The sampling theory, testing of hypothesis and making statistical inferences
- Stochastic process and Markov chains.

Course Outcomes: The student will learn

- Apply the concepts of probability and distributions to case studies.
- Formulate and solve problems involving random variables and apply statistical methods for analyzing experimental data.
- Apply concept of estimation and testing of hypothesis to case studies.
- Correlate the concepts of one unit to the concepts in other units.

UNIT - I: Probability:

Sample Space, Events, Counting Sample Points, Probability of an Event, Additive Rules, Conditional Probability, Independence, and the Product Rule, Baye's Rule, Random Variables and Probability Distributions: Concept of a Random Variable, Discrete Probability Distributions, Continuous Probability Distributions

UNIT - II: Expectation and discrete distributions

Mean of a Random Variable, Variance and Covariance of Random Variables, Means and Variances of Linear Combinations of Random Variables, Chebyshev's Theorem. Discrete Probability Distributions: Binomial Distribution, Poisson distribution.

UNIT - III: Continuous and Sampling Distributions

Uniform Distribution, Normal Distribution, Areas under the Normal Curve, Applications of the Normal Distribution, Normal Approximation to the Binomial Distributions. Fundamental Sampling Distributions: Random Sampling, Some Important Statistics, Sampling Distributions, Sampling Distribution of Means and the Central Limit Theorem, t - Distribution, F- Distribution.

UNIT - IV: Sample Estimation & Tests of Hypotheses

Introduction, Statistical Inference, Classical Methods of Estimation, Single Sample: Estimating the mean, standard error of a point estimate, prediction interval. Two sample: Estimating the difference between two means, Single sample: Estimating a proportion, Two samples: Estimating the difference between two proportions, Two samples: Estimating the ratio of two variances. Statistical Hypotheses: General Concepts, Testing a Statistical Hypothesis, Single sample: Tests concerning a single mean, Two samples: tests on two means, One sample: test on a single proportion. Two samples: tests on two

proportions, Two- sample tests concerning variances.

UNIT - V: Stochastic Processes and Markov Chains

Introduction to Stochastic processes- Markov process. Transition Probability, Transition Probability Matrix, First order and Higher order Markov process, n-step transition probabilities, Markov chain, Steady state condition, Markov analysis.

Text Books:

1. Ronald E. Walpole, Raymond H. Myers, Sharon L. Myers, Keying Ye, Probability & Statistics For Engineers & Scientists, 9th Ed. Pearson Publishers.
2. S C Gupta and V K Kapoor, Fundamentals of Mathematical statistics, Khanna publications.
3. S.D.Sharma, Operations Research, Kedarnath and Ramnath Publishers, Meerut, Delhi.

Reference Books:

1. T.T. Soong, Fundamentals of Probability and Statistics For Engineers, John Wiley & Sons, Ltd, 2004.
2. Sheldon M Ross, Probability and statistics for Engineers and scientists, academic press.
3. Miller and Freund's, Probability and Statistics for Engineers, 8th Edition, Pearson Educations.

B. Tech. IT	L	T	P	C
II Year - I Semester	3	0	0	3
COMPUTER ORGANIZATION AND MICROPROCESSOR				

Course Objectives:

1. To understand basic components of computers.
2. To understand the architecture of the 8086 processor.
3. To understand the instruction sets, instruction formats and various addressing modes of 8086.
4. To understand the representation of data at the machine level and how computations are performed at machine level.
5. To understand the memory organization and I/O organization.
6. To understand the parallelism both in terms of single and multiple processors.

Course Outcomes: The student will learn

1. Able to understand the basic components and the design of CPU, ALU and Control Unit.
2. Ability to understand memory hierarchy and its impact on computer cost/performance.
3. Ability to understand the advantage of instruction level parallelism and pipelining for high performance Processor design.
4. Ability to understand the instruction set, instruction formats and addressing modes of 8086.
5. Ability to write assembly language programs to solve problems.

UNIT - I: Digital Computers:

Introduction, Block diagram of Digital Computer, Definition of Computer Organization, Computer Design and Computer Architecture.

Basic Computer Organization and Design: Instruction codes, Computer Registers, Computer instructions, Timing and Control, Instruction cycle, Memory Reference Instructions, Input – Output and Interrupt, Complete Computer Description.

UNIT - II: Central Processing Unit:

The 8086 Processor Architecture, Register organization, Physical memory organization, General Bus Operation, I/O Addressing Capability, Special Processor Activities, Minimum and Maximum mode system and timings.

UNIT - III:

Assembly Language Programming with 8086- Machine level programs, Machine coding the programs, Programming with an assembler, Assembly Language example programs. Stack structure of 8086, Interrupts and Interrupt service routines, Interrupt cycle of 8086, Interrupt programming, Passing parameters to procedures, Macros, Timings and Delays.

UNIT - IV: Computer Arithmetic:

Introduction, Addition and Subtraction, Multiplication Algorithms, Division Algorithms, Floating - point Arithmetic operations. Input-Output Organization: Peripheral Devices, Input Output Interface, Asynchronous data transfer, Modes of Transfer, Priority Interrupt, Direct memory Access, Input –Output Processor (IOP), Intel 8089 IOP.

UNIT - V: Memory Organization:

Memory Hierarchy, Main Memory, Auxiliary memory, Associate Memory, Cache Memory. Pipeline and Vector Processing: Parallel Processing, Pipelining, Arithmetic Pipeline, Instruction Pipeline, RISC Pipeline, Vector Processing, Array Processors.

Text Books:

1. Computer System Architecture, M. Morris Mano, Third Edition, Pearson. (UNITS – I, IV, V)
2. Advanced Microprocessors and Peripherals, K M Bhurchandi, A. K Ray ,3rd edition, McGraw Hill India Education Private Ltd. (UNITS - II, III)

Reference Books:

1. Microprocessors and Interfacing, D V Hall, SSSP Rao, 3 rd edition, McGraw Hill India Education Private Ltd.
2. Carl Hamacher, Zvonko Vranesic, Safwat Zaky: Computer Organization, 5th Edition, Tata McGraw Hill, 2002.
3. Computer Organization and Architecture, William Stallings, 9th Edition, Pearson.
4. David A. Patterson, John L. Hennessy: Computer Organization and Design – The Hardware / Software Interface ARM Edition, 4th Edition, Elsevier, 2009.

B. Tech. IT**L T P C****II Year - I Semester****2 0 0 2****INTRODUCTION TO IOT****Course Objectives:**

- Understand the concepts of Internet of Things and able to build IoT applications
- Learn the programming and use of Arduino and Raspberry Pi boards.
- Known about data handling and analytics in SDN.

Course Outcomes: The student will learn

- Known basic protocols in sensor networks.
- Program and configure Arduino boards for various designs.
- Python programming and interfacing for Raspberry Pi.
- Explore IoT applications in different domains.

UNIT - I:

Introduction to Internet of Things, Characteristics of IoT, Physical design of IoT, Functional blocks of IoT, Sensing, Actuation, Basics of Networking, Communication Protocols, Sensor Networks.

UNIT - II:

Machine-to-Machine Communications, Difference between IoT and M2M, Interoperability in IoT, Introduction to Arduino Programming, Integration of Sensors and Actuators with Arduino.

UNIT - III:

Introduction to Python programming, Introduction to Raspberry Pi, Interfacing Raspberry Pi with basic peripherals, Implementation of IoT with Raspberry Pi, Case studies.

UNIT - IV:

Implementation of IoT with Raspberry Pi, Introduction to Software defined Network (SDN), SDN for IoT, Data Handling and Analytics.

UNIT - V:

Cloud Computing, Sensor-Cloud, Smart Cities and Smart Homes, Connected Vehicles, Smart Grid, Industrial IoT. Case Study: Agriculture, Healthcare, Activity Monitoring

Text Books:

1. Pethuru Raj and Anupama C. Raman "The Internet 'of Things: Enabling Technologies, Platforms, and Use Cases", (CRC Press)
2. Terokarvinen, kemo, karvinen and villey valtokari, "Make sensors": 1st edition, maker media, 2014.

Reference Books:

1. Vijay Madiseti, Arshdeep Bahga, "Internet of Things: A Hands-On Approach"
2. Waltenegus Dargie, Christian Poellabauer, "Fundamentals of Wireless Sensor Networks: Theory and Practice"
3. Beginning Sensor networks with Arduino and Raspberry Pi – Charles Bell, Apress, 2013

B. Tech. IT	L	T	P	C
II Year - I Semester	0	0	2	1

DIGITAL ELECTRONICS LAB

Course Objectives:

- **Understand** numerical information representation in different number systems and Boolean algebra theorems.
- **Learn** the fundamental postulates of Boolean algebra and techniques for minimizing combinational functions.
- **Design and analyze** combinational circuits such as adders, encoders, and multiplexers.
- **Develop and evaluate** sequential circuits, including flip-flops, counters, and shift registers for cyclic functions.
- **Characterize and analyze** logic families based on AC and DC parameters for digital circuit applications.

Course Outcomes: The student will learn

- Acquire the knowledge on numerical information in different forms and Boolean Algebra Theorems.
- Define Postulates of Boolean algebra and to minimize combinational functions, and design the combinational circuits.
- Design and Analyze Sequential Circuits for various cyclic functions.
- Characterize logic families analyze them for the purpose of AC and DC parameters.

LIST OF EXPERIMENTS:

1. Realization of Logic circuit to generate r's Complement using Logic Gates.
2. Realization of given Boolean function using universal gates and minimizing the same. Compare the gate count before and after minimization.
3. Design and realize Full Adder circuit using gates/universal gates. Implement Full Subtractor using full adder.
4. Designing a 2 – bit Comparator using AND, OR and NOT gates. Realize 4 – bit Comparator using 2 – bit Comparators.
5. Realize 2:1 MUX using the given gates and Design 8:1 using 2:1 MUX.
6. Implement the given Boolean function using the given MUX(ex: code converters).
7. Realize a 2x4 Decoder using logic gates and implement 3x8 Decoder using 2x4 Decoder.
8. Implement the given Boolean function using given Decoders.
9. Convert Demultiplexer to Decoder and vice versa.
10. Verification of truth tables of flip flops using different clocks (level triggering, positive and negative edge triggering) also converts the given flip flop from one type to another.
11. Designing of Universal n-bit shift register using flip flops and Multiplexers. Draw the timing diagram of the Shift Register.

12. Design a Synchronous binary counter using D-flipflop /given flip flop.
13. Design Asynchronous counter for the given sequence using given flip flops.
14. Designing of MOD 8 Counter using JK flip flops.

Major Equipment required for Laboratories:

1. 5 Fixed Regulated Power Supply/ 0-5V or more Regulated Power Supply.
2. 20 MHz Oscilloscope with Dual Channel.
3. Bread board and components/ Trainer Kit.
4. Multimeter.

B. Tech. IT**L T P C****II Year - I Semester****0 0 3 1.5****DATA STRUCTURES LAB****Course Objectives:**

- It covers various concepts of C programming language
- It introduces searching and sorting algorithms
- It provides an understanding of data structures such as stacks and queues.

Course Outcomes: The student will learn

- Ability to develop C programs for computing and real-life applications using basic elements like control statements, arrays, functions, pointers and strings, and data structures like stacks, queues and linked lists.
- Ability to Implement searching and sorting algorithms

LIST OF EXPERIMENTS:

1. Write a program that uses functions to perform the following operations on singly linked list.:
i) Creation ii) Insertion iii) Deletion iv) Traversal
2. Write a program that uses functions to perform the following operations on doubly linked list.:
i) Creation ii) Insertion iii) Deletion iv) Traversal
3. Write a program that uses functions to perform the following operations on circular linked list.:
i) Creation ii) Insertion iii) Deletion iv) Traversal
4. Write a program that implement stack (its operations) using
i) Arrays ii) Pointers
5. Write a program that implement Queue (its operations) using
i) Arrays ii) Pointers
6. Write a program that implements the following sorting methods to sort a given list of integers in ascending order
i) Quick sort ii) Heap sort iii) Merge sort
7. Write a program to implement the tree traversal methods(Recursive and Non Recursive).
8. Write a program to implement
i) Binary Search tree ii) B Trees iii) B+ Trees iv) AVL trees v) Red - Black trees
9. Write a program to implement the graph traversal methods.
10. Implement a Pattern matching algorithms using Boyer- Moore, Knuth-Morris-Pratt

Text Books:

1. Fundamentals of Data Structures in C, 2nd Edition, E. Horowitz, S. Sahni and Susan Anderson Freed, Universities Press.
2. Data Structures using C – A. S. Tanenbaum, Y. Langsam, and M. J. Augenstein, PHI/Pearson Education.

Reference Books:

1. Data Structures: A Pseudocode Approach with C, 2nd Edition, R. F. Gilberg and B. A. Forouzan, Cengage Learning.

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B. Tech. IT		L	T	P	C
II Year - I Semester		0	0	3	1.5

INTERNET OF THINGS LAB

Course Objectives:

- To introduce the raspberry PI platform, that is widely used in IoT applications
- To introduce the implementation of distance sensor on IoT devices

Course Outcomes: The student will learn

- Ability to introduce the concept of M2M (machine to machine) with necessary protocols and get awareness in implementation of distance sensor
- Get the skill to program using python scripting language which is used in many IoT devices

List of Experiments

1. Using raspberry pi a. Calculate the distance using a distance sensor. b. Basic LED functionality.
2. Using Arduino a. Calculate the distance using a distance sensor. b. Basic LED functionality. c. Calculate temperature using a temperature sensor.
3. Using Node MCU
 - a. Calculate the distance using a distance sensor.
 - b. Basic LED functionality.
 - c. Calculate temperature using a temperature sensor.
4. Installing OS on Raspberry Pi
 - a) Installation using PiImager
 - b) Installation using image file
 - Downloading an Image
 - Writing the image to an SD card using Linux using Windows
 - Booting up Follow the instructions given in the URL <https://www.raspberrypi.com/documentation/computers/getting-started.html>
5. Accessing GPIO pins using Python
 - a) Installing GPIO Zero library. First, update your repositories list: `sudo apt update` Then install the package for Python 3: `sudo apt install python3-gpiozero`
 - b) Blinking an LED connected to one of the GPIO pin
 - c) Adjusting the brightness of an LED Adjust the brightness of an LED (0 to 100, where 100 means maximum brightness) using the in-built PWM wavelength.
6. Collecting Sensor Data
 - a) DHT Sensor interface
 - Connect the terminals of DHT GPIO pins of Raspberry Pi.
 - Import the DHT library using `import Adafruit_DHT`
 - Read sensor data and display it on screen.

Text Books:

1. Internet of Things - A Hands-on Approach, Arshdeep Bahga and Vijay Madisetti, Universities Press, 2015, ISBN: 9788173719547
2. Getting Started with Raspberry Pi, Matt Richardson & Shawn Wallace, O'Reilly (SPD), 2014, ISBN: 9789350239759

Reference Books:

1. Bernd Scholz-Reiter, Florian Michahelles, "Architecting the Internet of Things", ISBN 978-3- 642-19156-5 e-ISBN 978-3-642-19157-2, Springer, 2016
2. N. Ida, Sensors, Actuators and Their Interfaces, Scitech Publishers, 2014.

B. Tech. IT**L T P C****II Year - I Semester****0 0 2 1****DATA VISUALIZATION - R PROGRAMMING/ POWER BI****Course Objectives:**

- Effective use of Business Intelligence (BI) technology (Tableau) to apply data visualization
- To discern patterns and relationships in the data.
- To build Dashboard applications.
- To communicate the results clearly and concisely.
- To be able to work with different formats of data sets

Course Outcomes: The student will learn

- Understand How to import data into Tableau.
- Understand Tableau concepts of Dimensions and Measures.
- Develop Programs and understand how to map Visual Layouts and Graphical Properties.
- Create a Dashboard that links multiple visualizations.
- Use graphical user interfaces to create Frames for providing solutions to real world problems.

Lab Problems:

1. Understanding Data, What is data, where to find data, Foundations for building Data Visualizations, Creating Your First visualization?
2. Getting started with Tableau Software using Data file formats, connecting your Data to Tableau, creating basic charts(line, bar charts, Tree maps),Using the Show me panel.
3. Tableau Calculations, Overview of SUM, AVR, and Aggregate features, Creating custom calculations and fields. \
4. Applying new data calculations to your visualizations, Formatting Visualizations, Formatting Tools and Menus, Formatting specific parts of the view.
5. Editing and Formatting Axes, Manipulating Data in Tableau data, Pivoting Tableau data.
6. Structuring your data, Sorting and filtering Tableau data, Pivoting Tableau data.
7. Advanced Visualization Tools: Using Filters, Using the Detail panel, using the Size panels, customizing filters, Using and Customizing tooltips, Formatting your data with colors.
8. Creating Dashboards & Storytelling, creating your first dashboard and Story, Design for different displays, adding interactivity to your Dashboard, Distributing & Publishing your Visualization.
9. Tableau file types, publishing to Tableau Online, Sharing your visualizations, printing, and Exporting.
10. Creating custom charts, cyclical data and circular area charts, Dual Axis charts.

Reference Books:

1. Microsoft Power BI cookbook, Brett Powell, 2nd edition.
2. R Programming for Data Science by Roger D. Peng (References)
3. The Art of R Programming by Norman Matloff Cengage Learning India. and Actuators with Arduino.

B. Tech. IT

L T P C

II Year - I Semester

0 0 2 0

GENDER SENSITIZATION LAB**Course Objectives:**

- To develop students' sensibility with regard to issues of gender in contemporary India.
- To provide a critical perspective on the socialization of men and women.
- To introduce students to information about some key biological aspects of genders.
- To expose the students to debates on the politics and economics of work.
- To help students reflect critically on gender violence.
- To expose students to more egalitarian interactions between men and women.

Course Outcomes: The student will learn

- Students will have developed a better understanding of important issues related to gender in contemporary India.
- Students will be sensitized to basic dimensions of the biological, sociological, psychological and legal aspects of gender. This will be achieved through discussion of materials derived from research, facts, everyday life, literature and film.
- Students will attain a finer grasp of how gender discrimination works in our society and how to— counter it.
- Students will acquire insight into the gendered division of labor and its relation to politics and economics.
- Men and women students and professionals will be better equipped to work and live together as equals.

Unit-I: UNDERSTANDING

GENDER Introduction: Definition of Gender-Basic Gender Concepts and Terminology-Exploring Attitudes towards Gender-Construction of Gender-Socialization: Making Women, Making Men - Preparing for Womanhood. Growing up Male. First lessons in Caste.

Unit – II: GENDER ROLES AND RELATIONS

Two or Many? -Struggles with Discrimination-Gender Roles and Relations-Types of Gender Roles Gender Roles and Relationships Matrix-Missing Women-Sex Selection and Its Consequences Declining Sex Ratio. Demographic Consequences-Gender Spectrum: Beyond the Binary

Unit – III: GENDER AND LABOUR

Division and Valuation of Labour-Housework: The Invisible Labor- “My Mother doesn't Work.” “Share the Load.”-Work: Its Politics and Economics -Fact and Fiction. Unrecognized and Unaccounted work. -Gender Development Issues-Gender, Governance

and Sustainable Development-Gender and Human Rights-Gender and Mainstreaming

Unit – IV: GENDER - BASED VIOLENCE The Concept of Violence- Types of Gender-based Violence-Gender-based Violence from a Human Rights Perspective-Sexual Harassment: Say No!-Sexual Harassment, not Eve-teasing- Coping with Everyday Harassment- Further Reading: “Chupulu”.

Domestic Violence: Speaking Out Is Home a Safe Place? -When Women Unite [Film]. Rebuilding Lives. Thinking about Sexual Violence Blaming the Victim-“I Fought for my Life....”

Unit – V: GENDER AND CULTURE

Gender and Film-Gender and Electronic Media-Gender and Advertisement-Gender and Popular Literature- Gender Development Issues-Gender Issues - Gender Sensitive Language- Gender and Popular Literature - Just Relationships: Being Together as Equals Mary Kom and Onler. Love and Acid just do not Mix. Love Letters. Mothers and Fathers. Rosa Parks-The Brave Heart.

Note: Since it is Interdisciplinary Course, Resource Persons can be drawn from the fields of English Literature or Sociology or Political Science or any other qualified faculty who has expertise in this field from engineering departments.

- *Classes will consist of a combination of activities: dialogue-based lectures, discussions, collaborative learning activities, group work and in-class assignments. Apart from the above prescribed book, Teachers can make use of any authentic materials related to the topics given in the syllabus on “Gender”.*
- **ESSENTIAL READING:** The Textbook, “Towards a World of Equals: A Bilingual Textbook on Gender” written by A.Suneetha, Uma Bhrugubanda, Duggirala Vasanta, Rama Melkote, Vasudha Nagaraj, Asma Rasheed, Gogu Shyamala, Deepa Sreenivas and Susie Tharu **published by Telugu Akademi, Telangana Government in 2015.**

ASSESSMENT AND GRADING:

- Discussion & Classroom Participation: 20%
- Project/Assignment: 30%
- End Term Exam: 50%